

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1744		SERIAL NO. 09/677,373	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary.)				APPLICANT Blalock et al.			
				FILING DATE October 2, 2000		GROUP 1765	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
LV	AA	6,338,053 B1	03/18/03	Allen, III			
	AB	5,368,333	02/09/99	Chen et al.			
	AC	5,365,938	02/02/99	Peters et al.			
	AD	5,343,841	12/01/98	Pu et al.			
	AE	5,076,666	06/25/91	Hills et al.			
LV	AF	6,434,327 B1	08/13/02	Greer et al.			
	AG						
	AH						
	AI						
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AL						
	AM						
	AN						
	AO						
	AP						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
LV	AR		Stanley Wolf and Richard Tauber, Silicon Processing for the VLSI Era, Vol. 1, pp 549, 1986.				
	AS						
	AT						
EXAMINER		LAN VINH			DATE CONSIDERED		
					3/4/2004		
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							